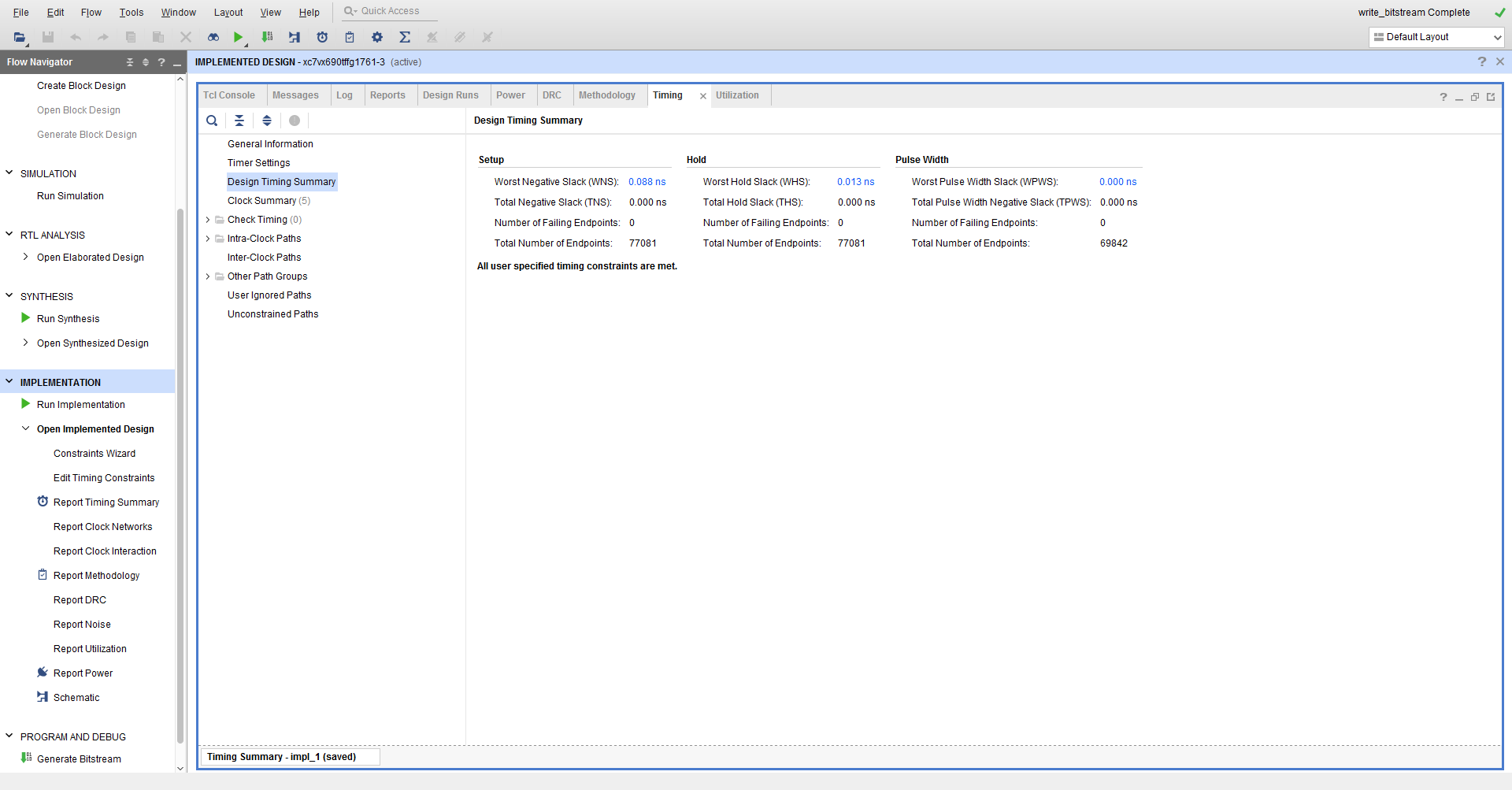
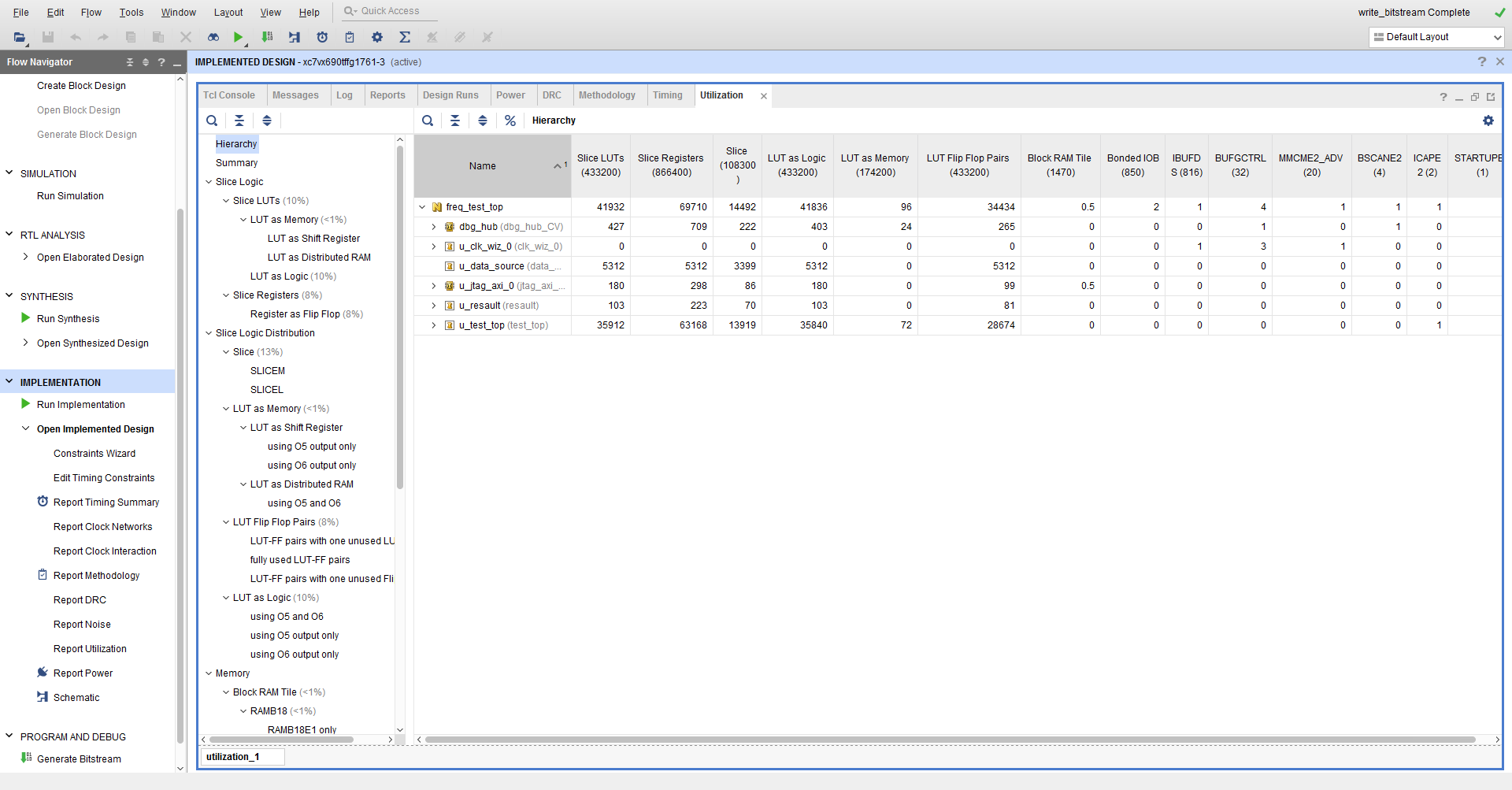
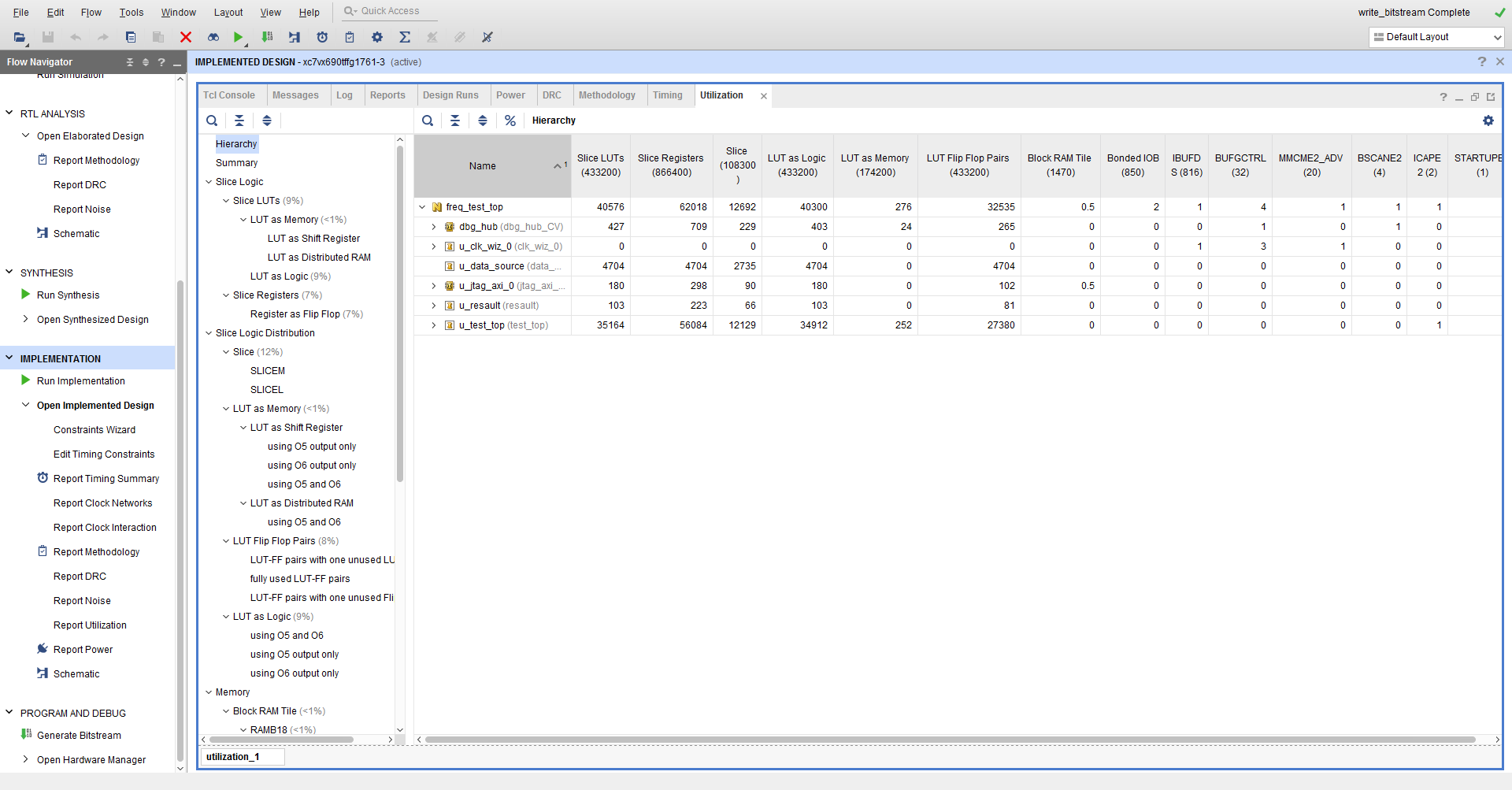
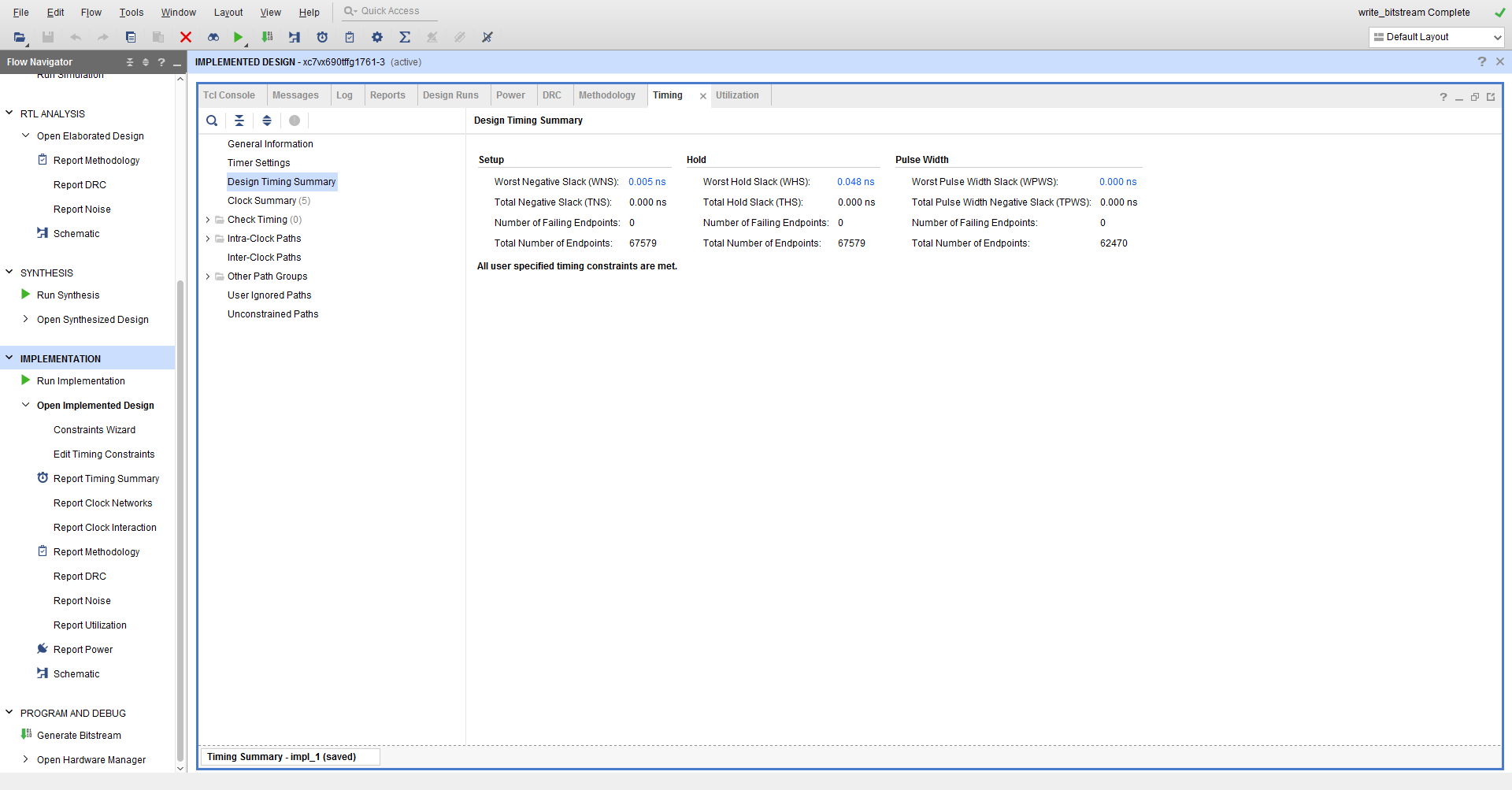
Bus Width：4096bit SEG Width：64bit



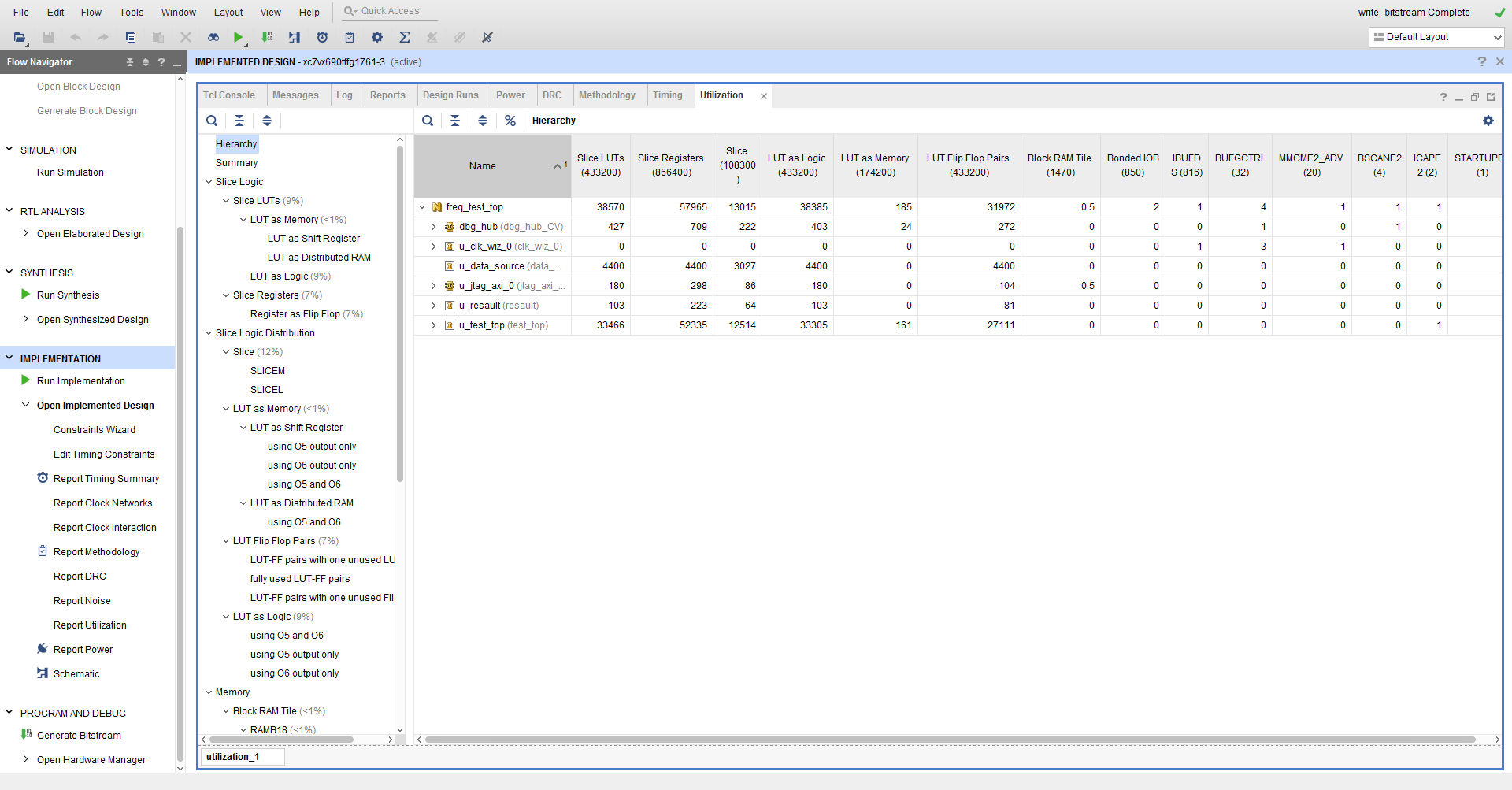


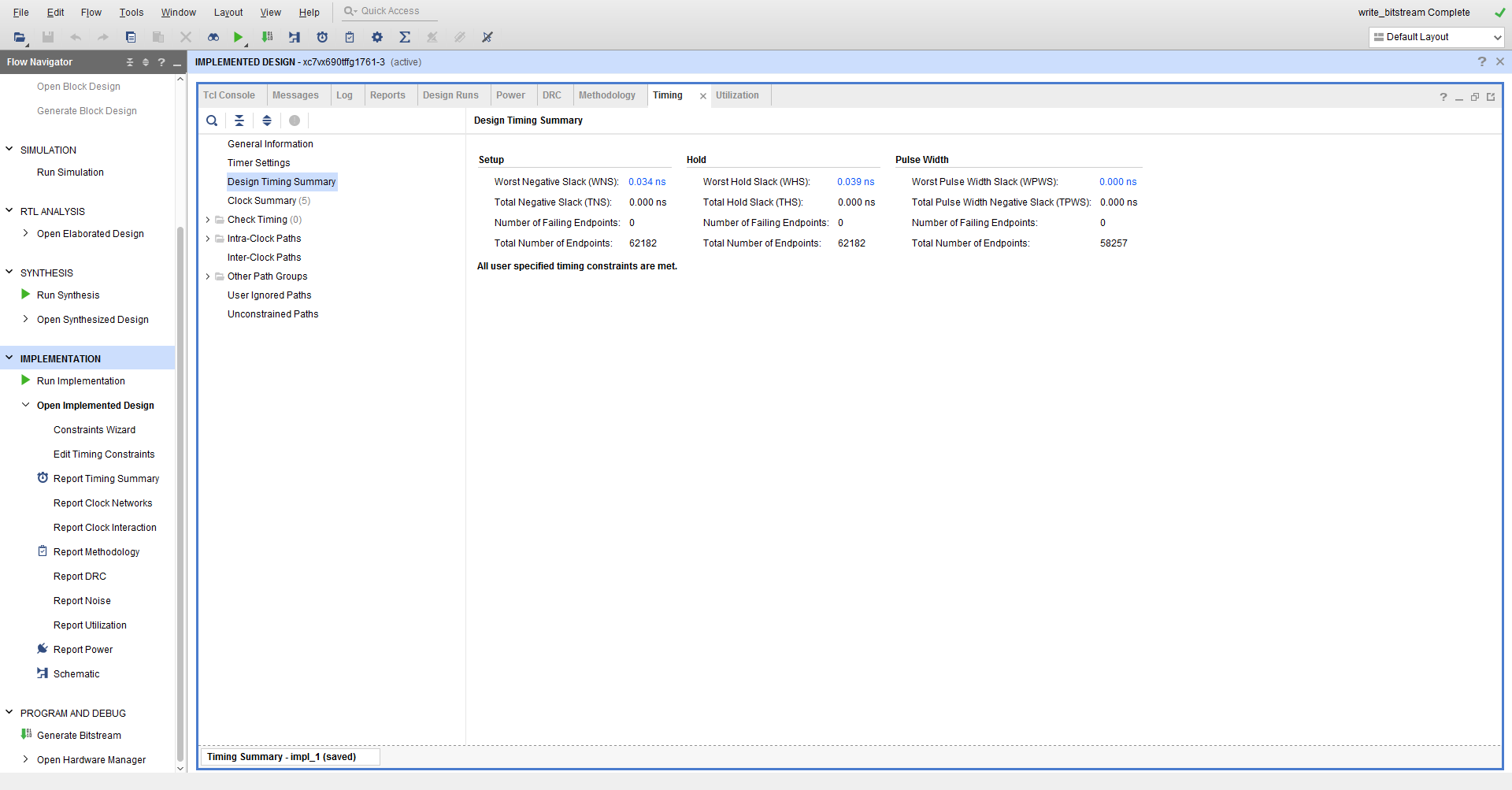
Bus Width：4096bit SEG Width：128bit



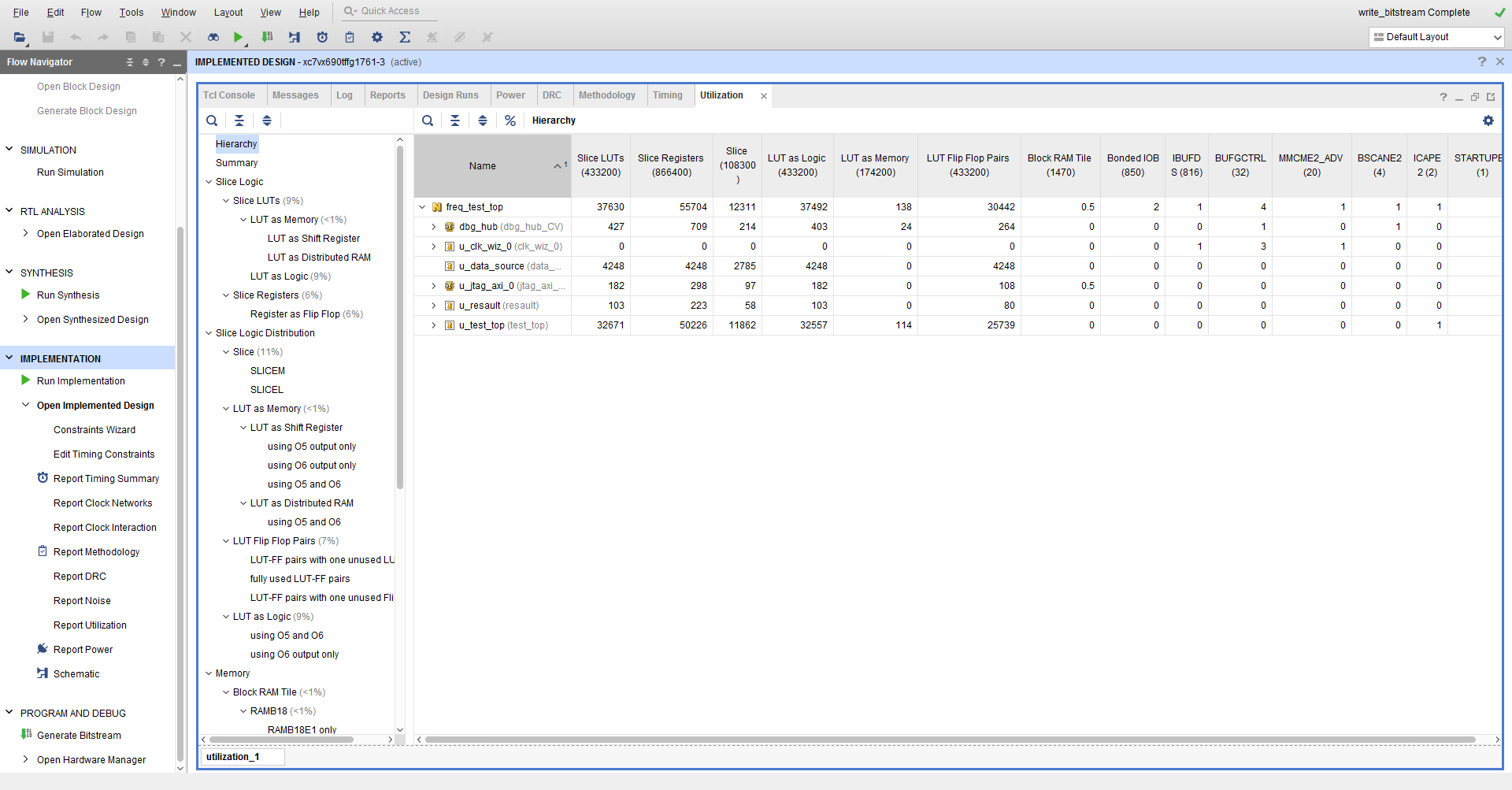


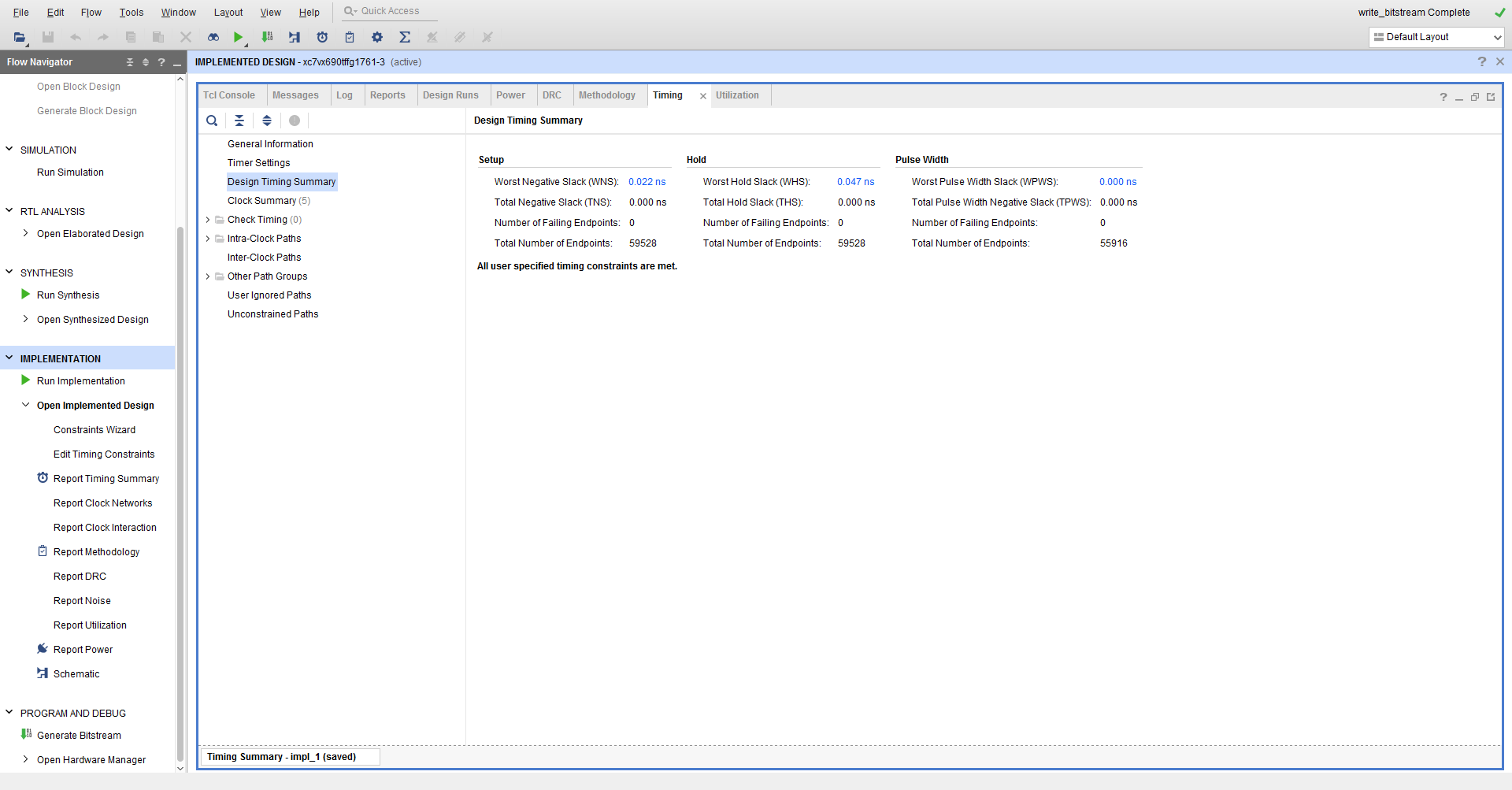
Bus Width：4096bit SEG Width：256bit



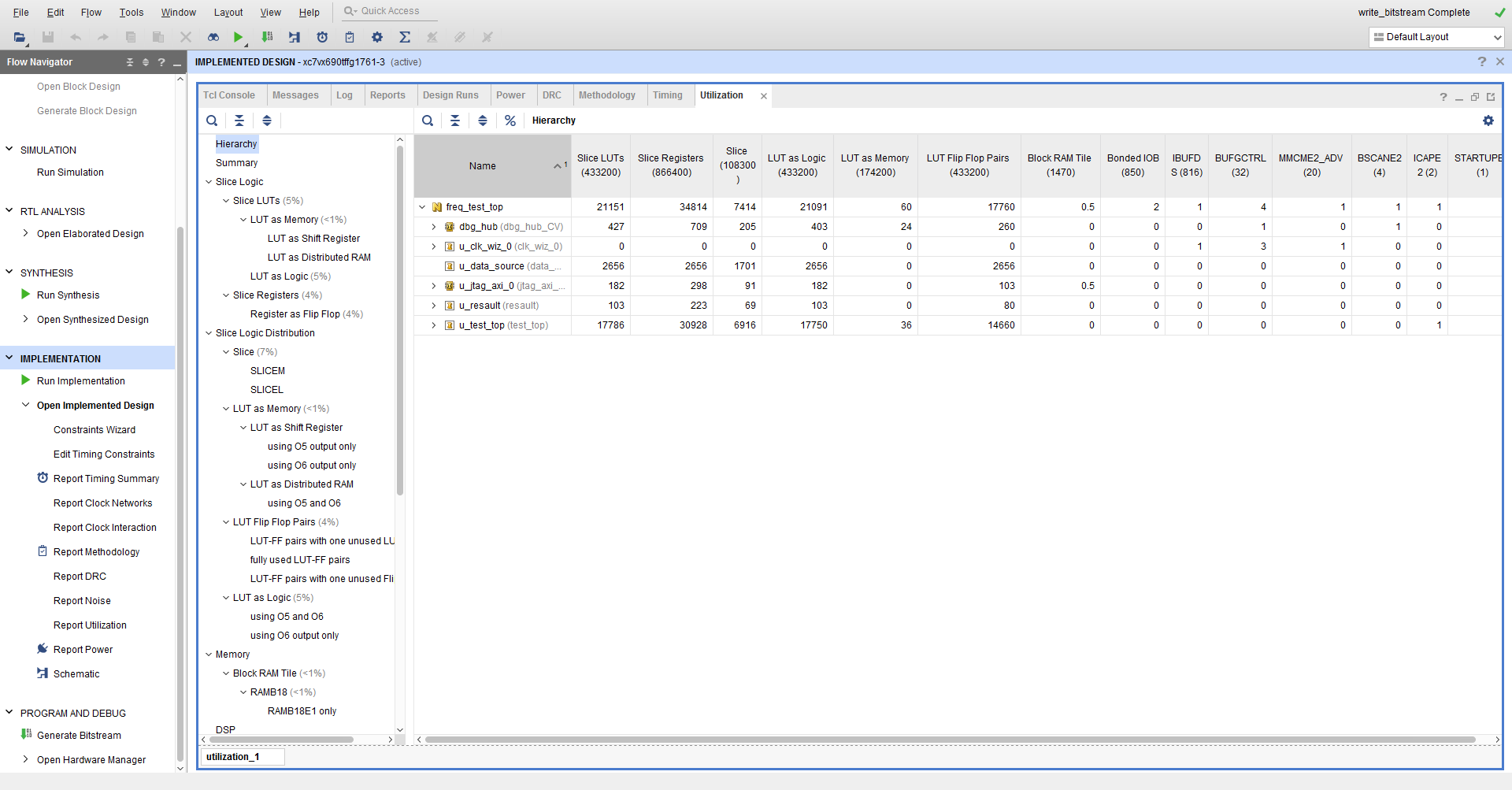


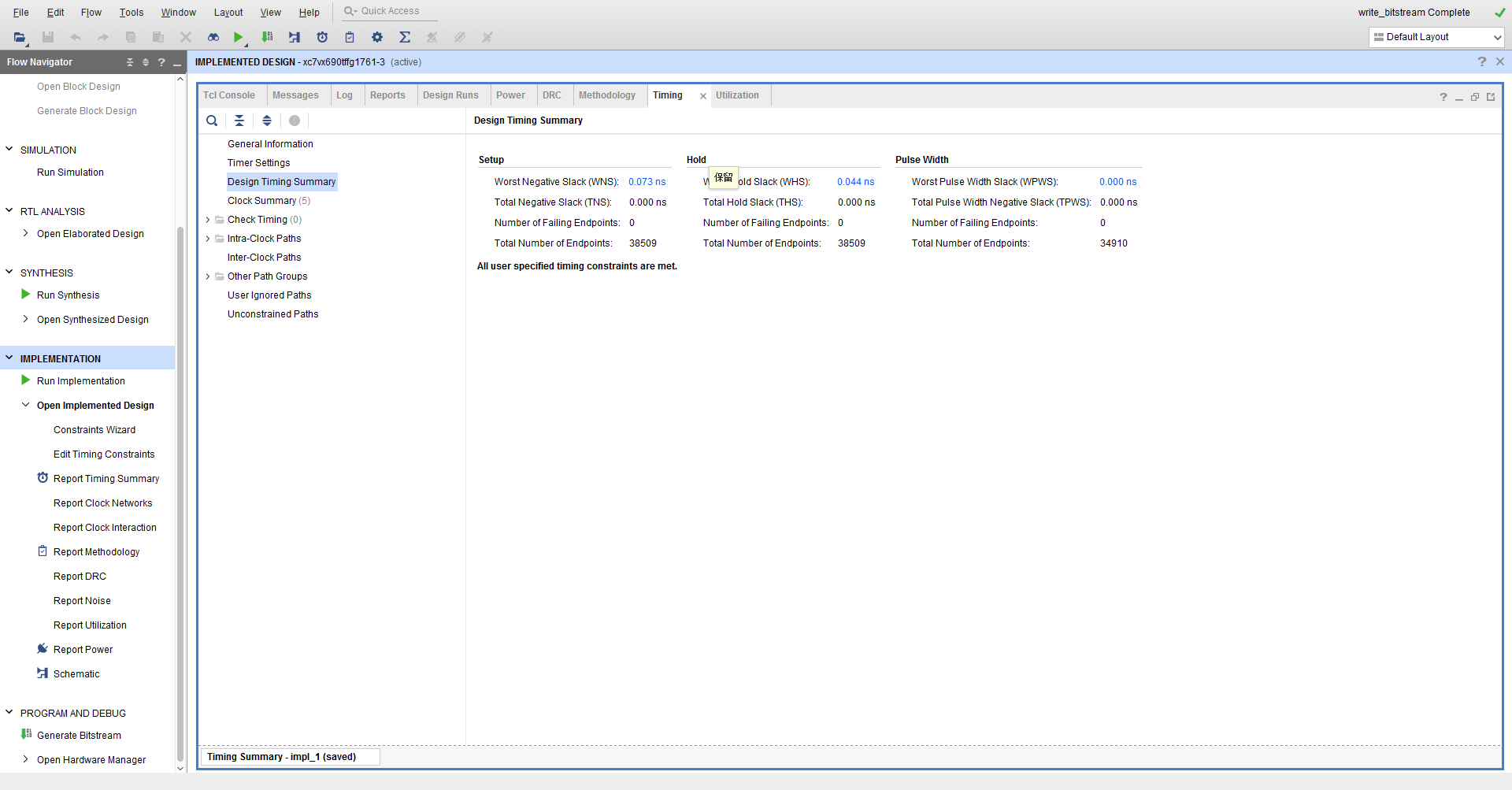
Bus Width：4096bit SEG Width：512bit



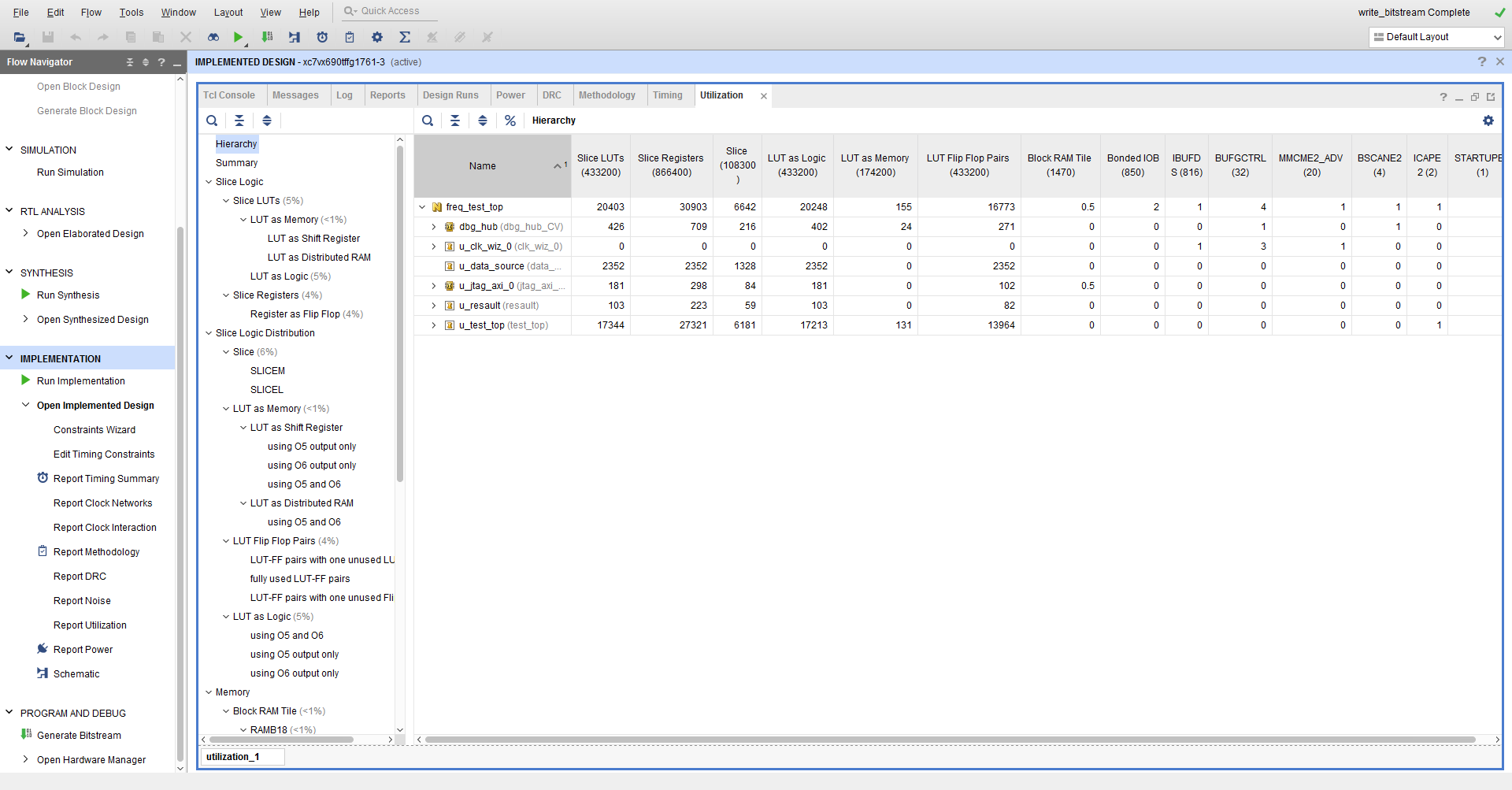


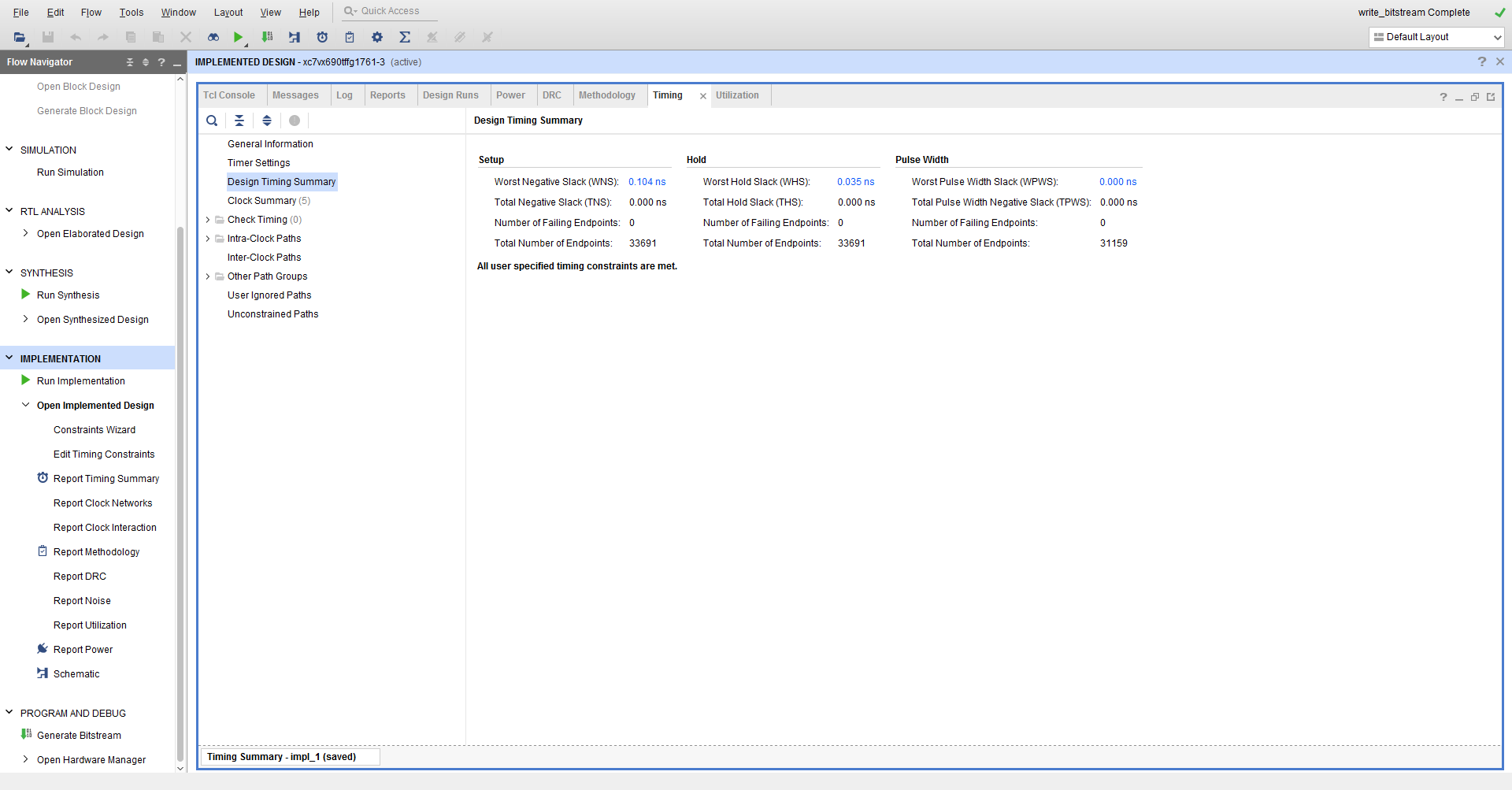
Bus Width：2048bit SEG Width：64bit



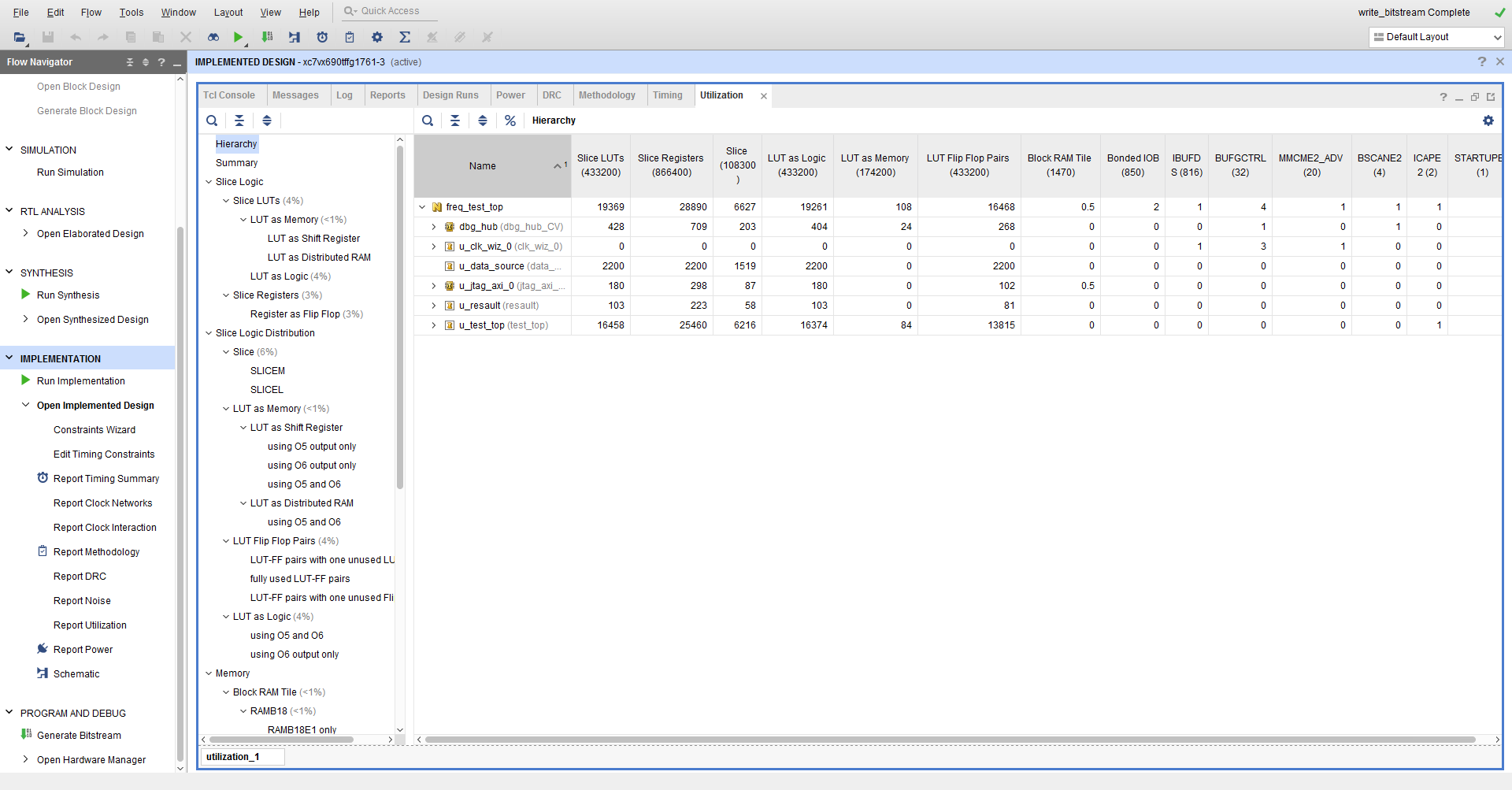


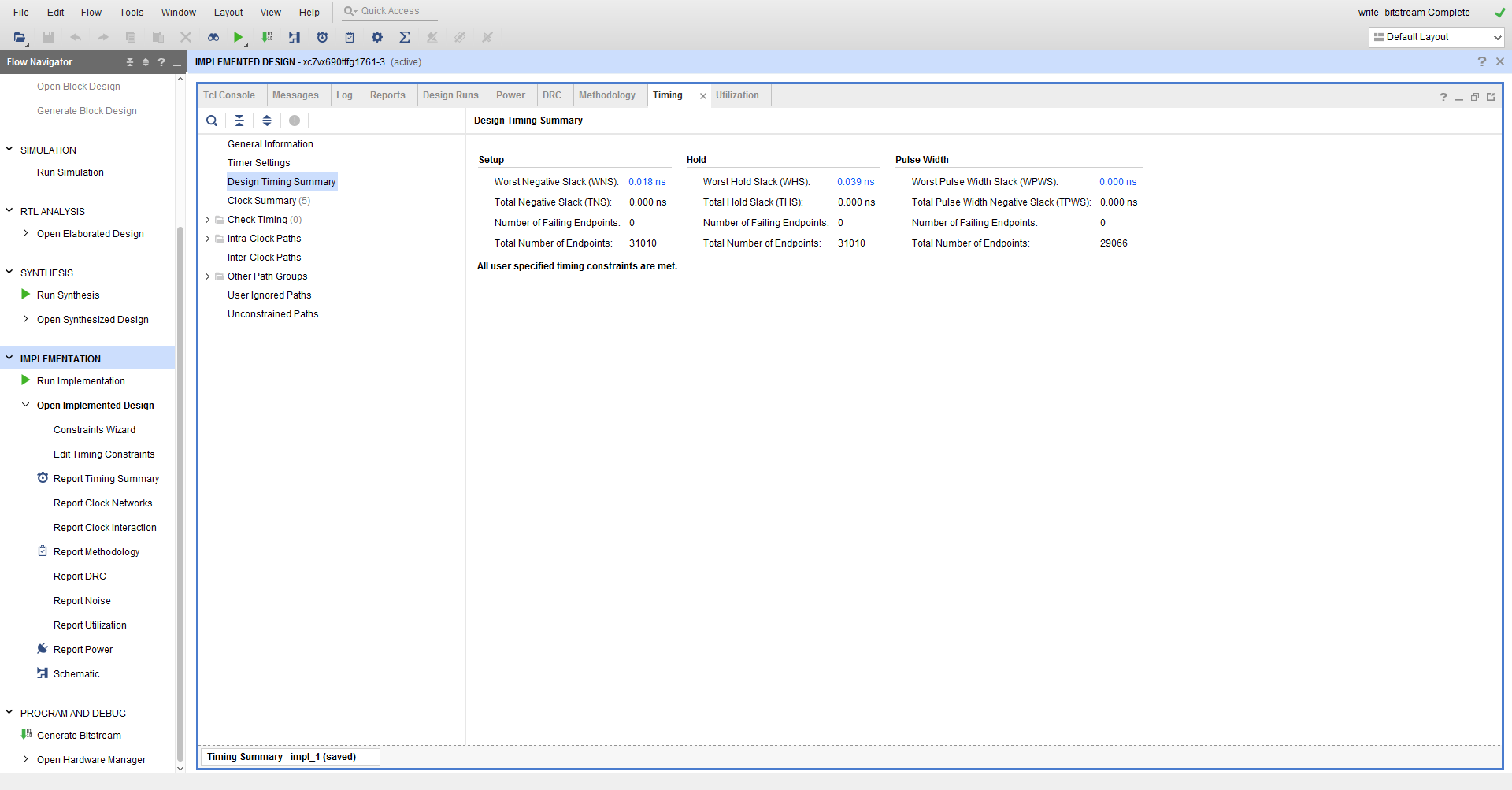
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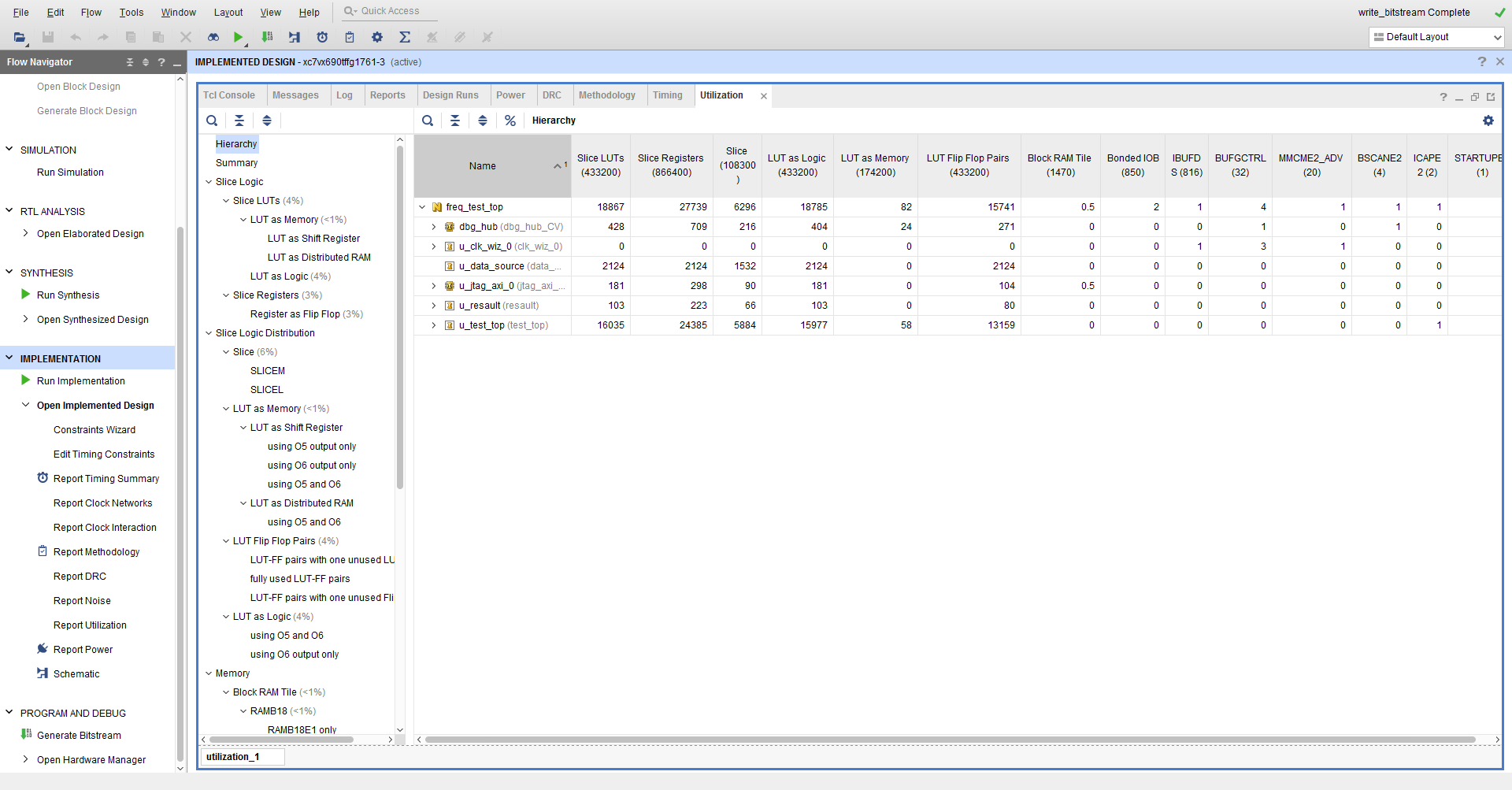


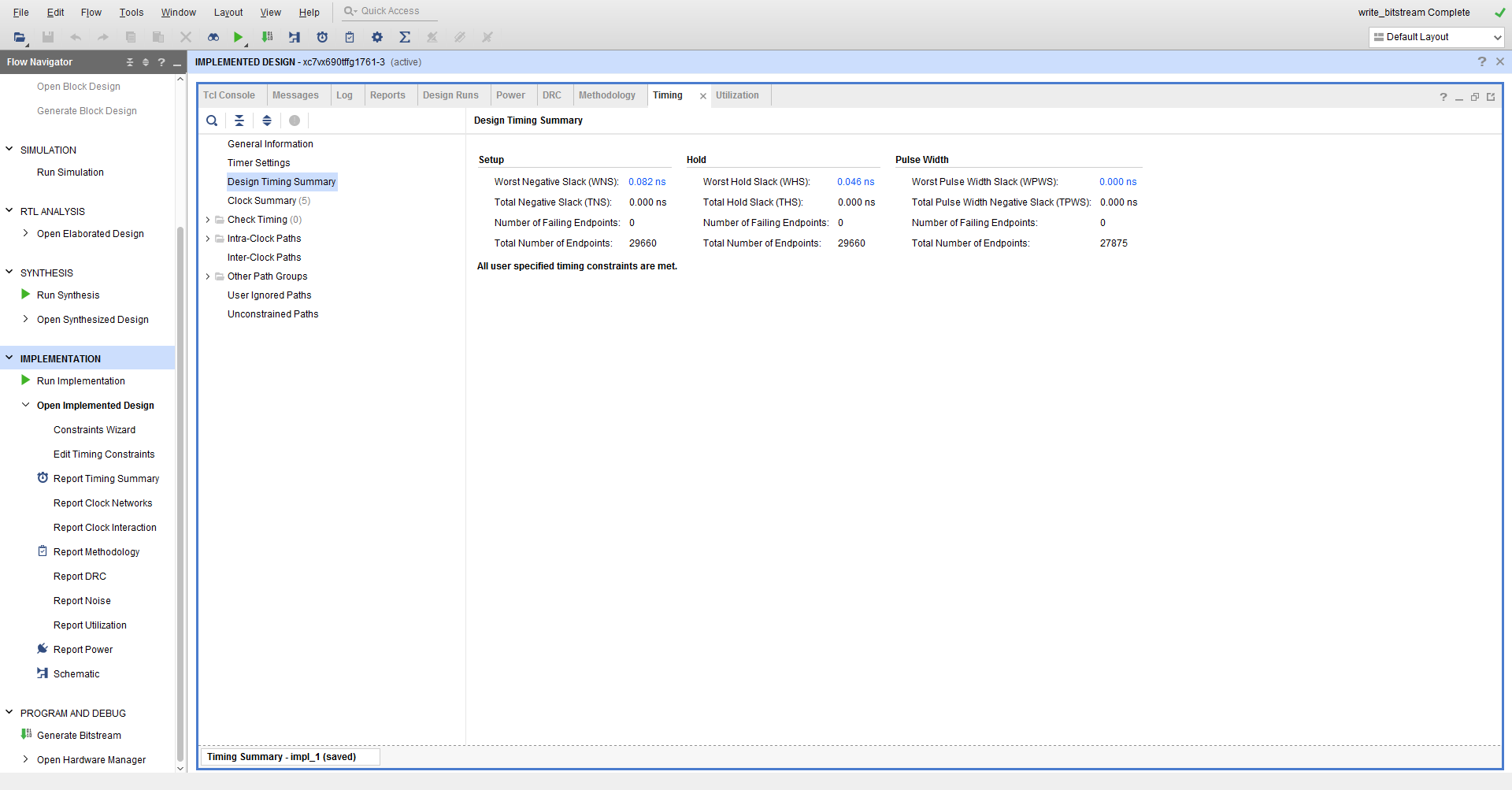
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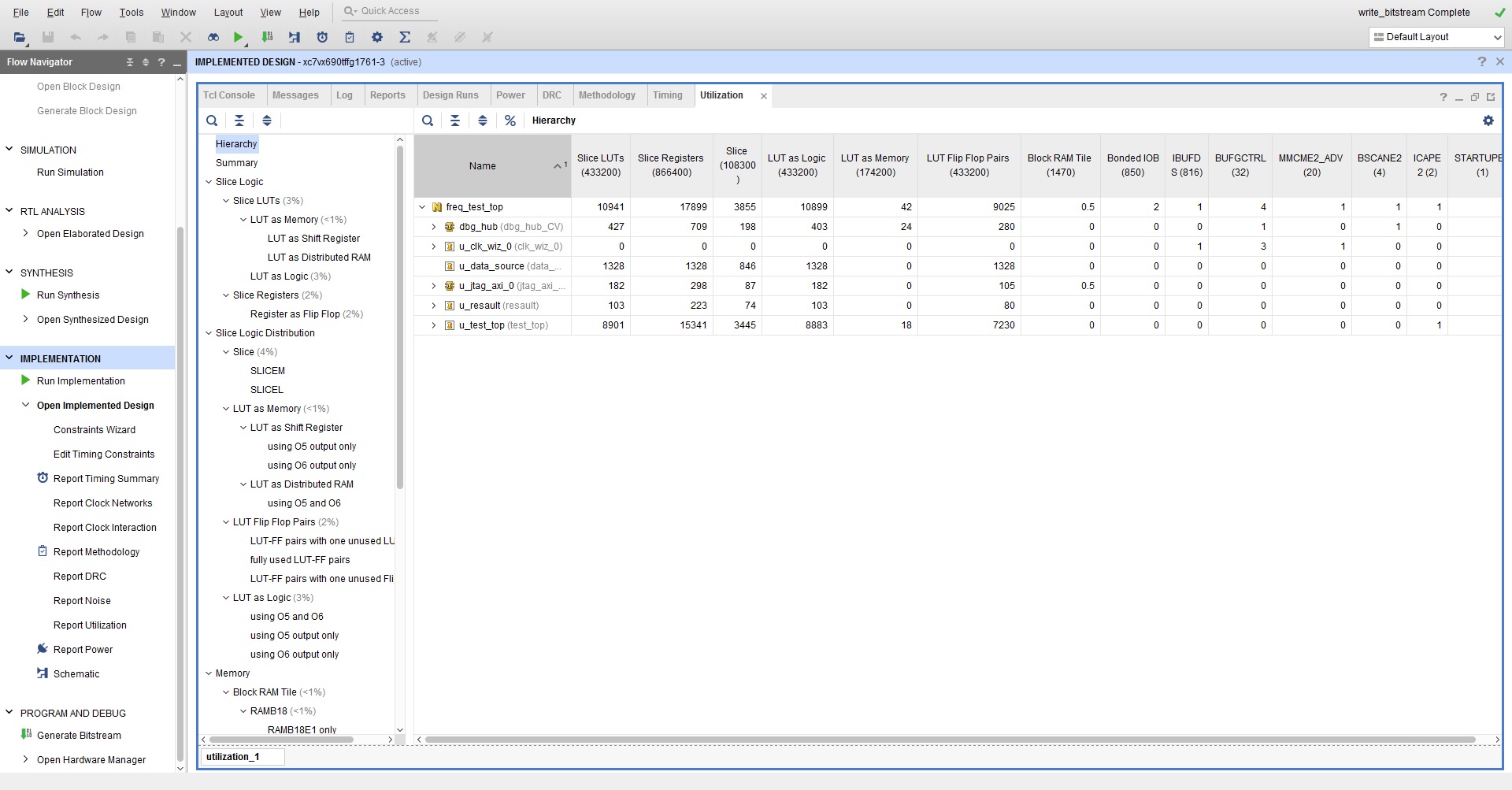


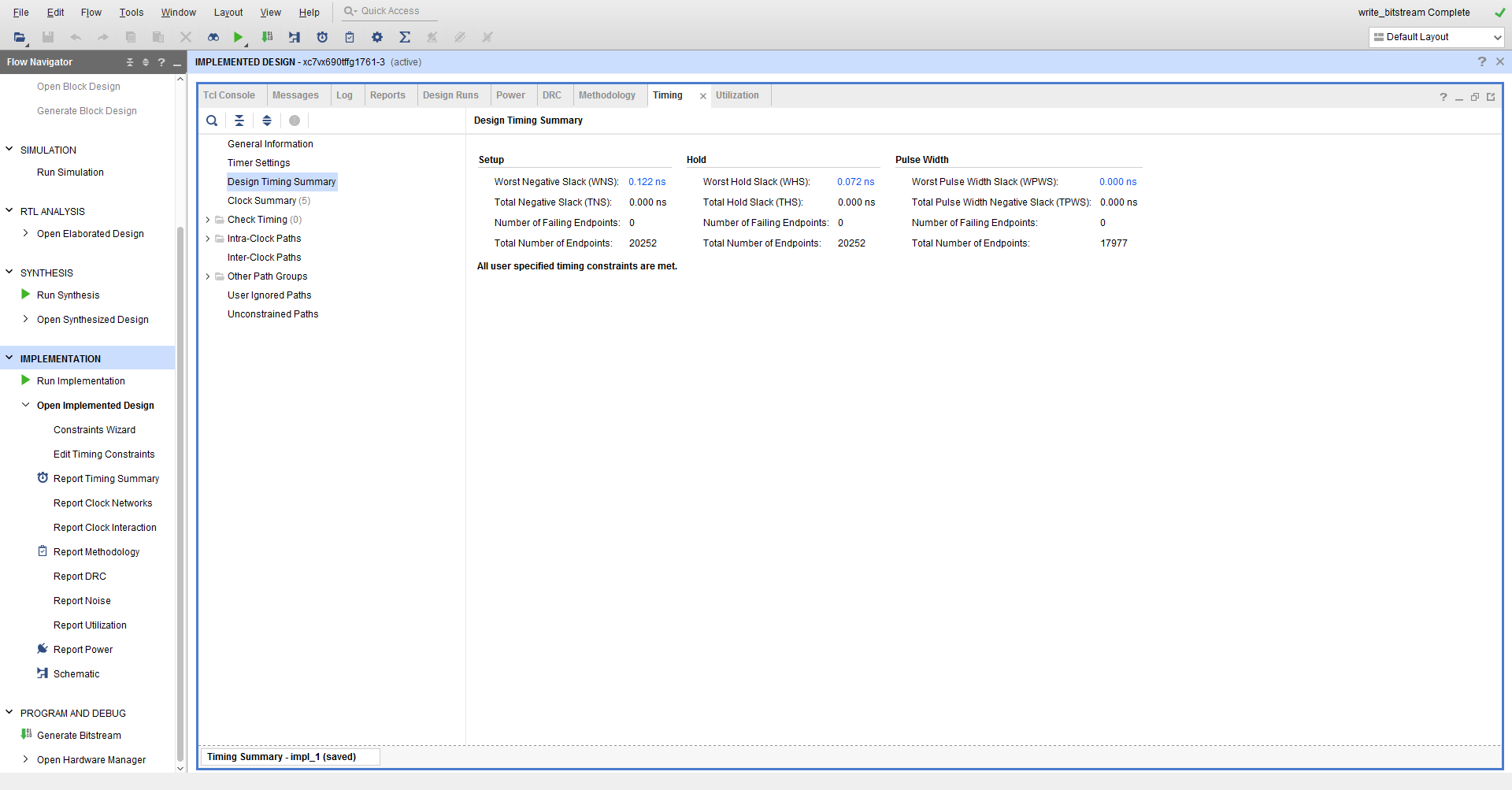
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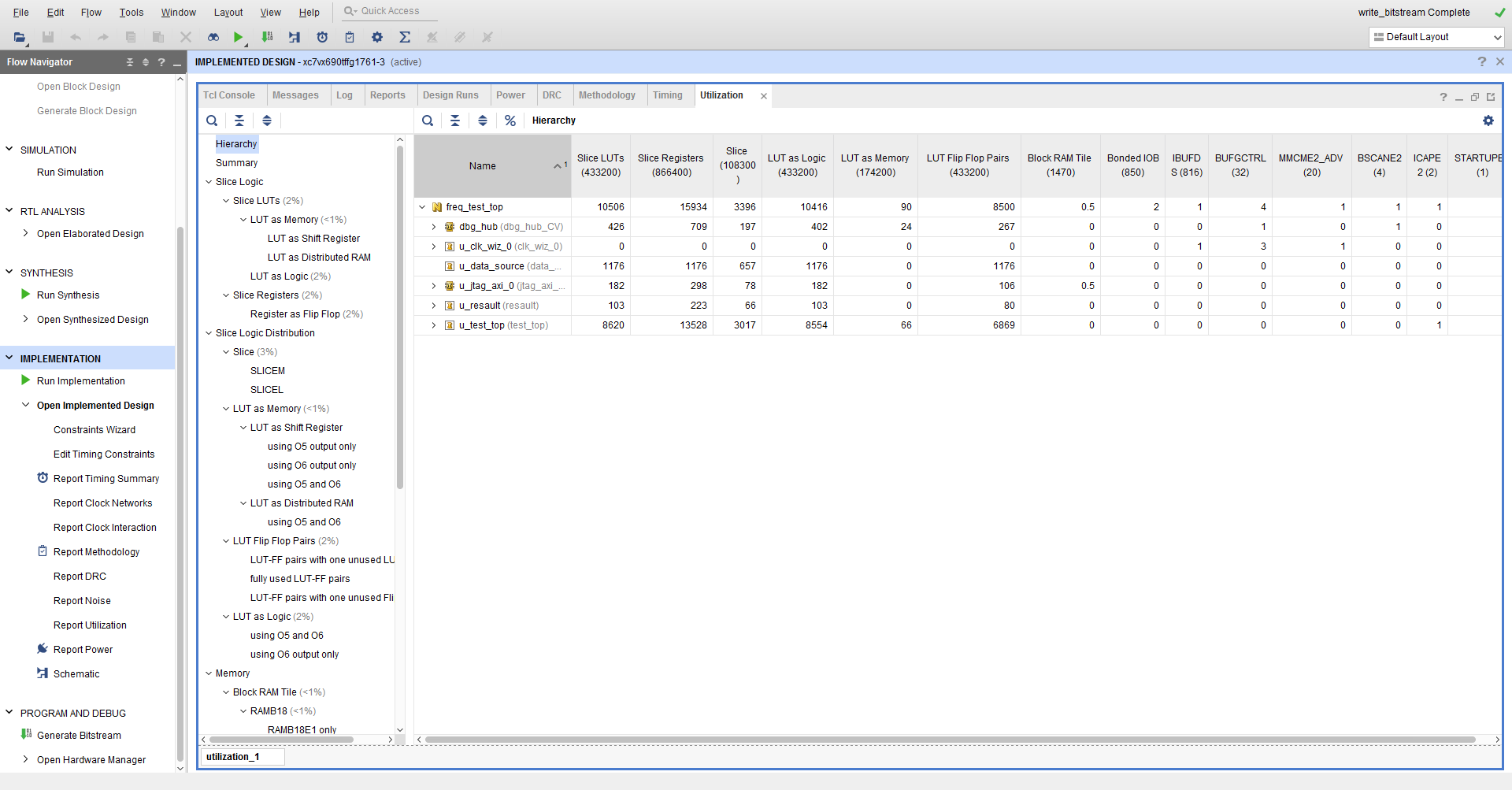


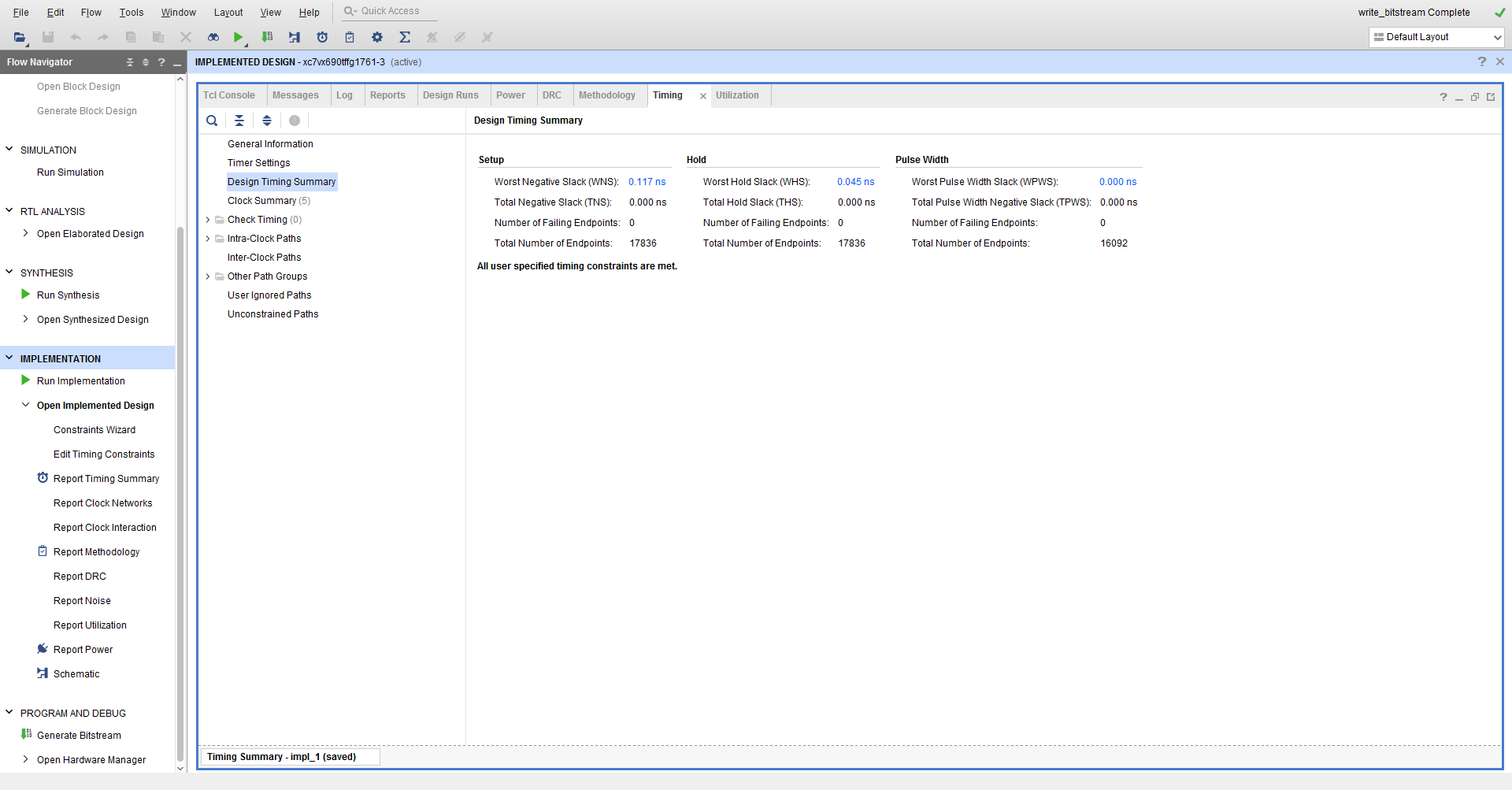
Bus Width：1024bit SEG Width：64bit



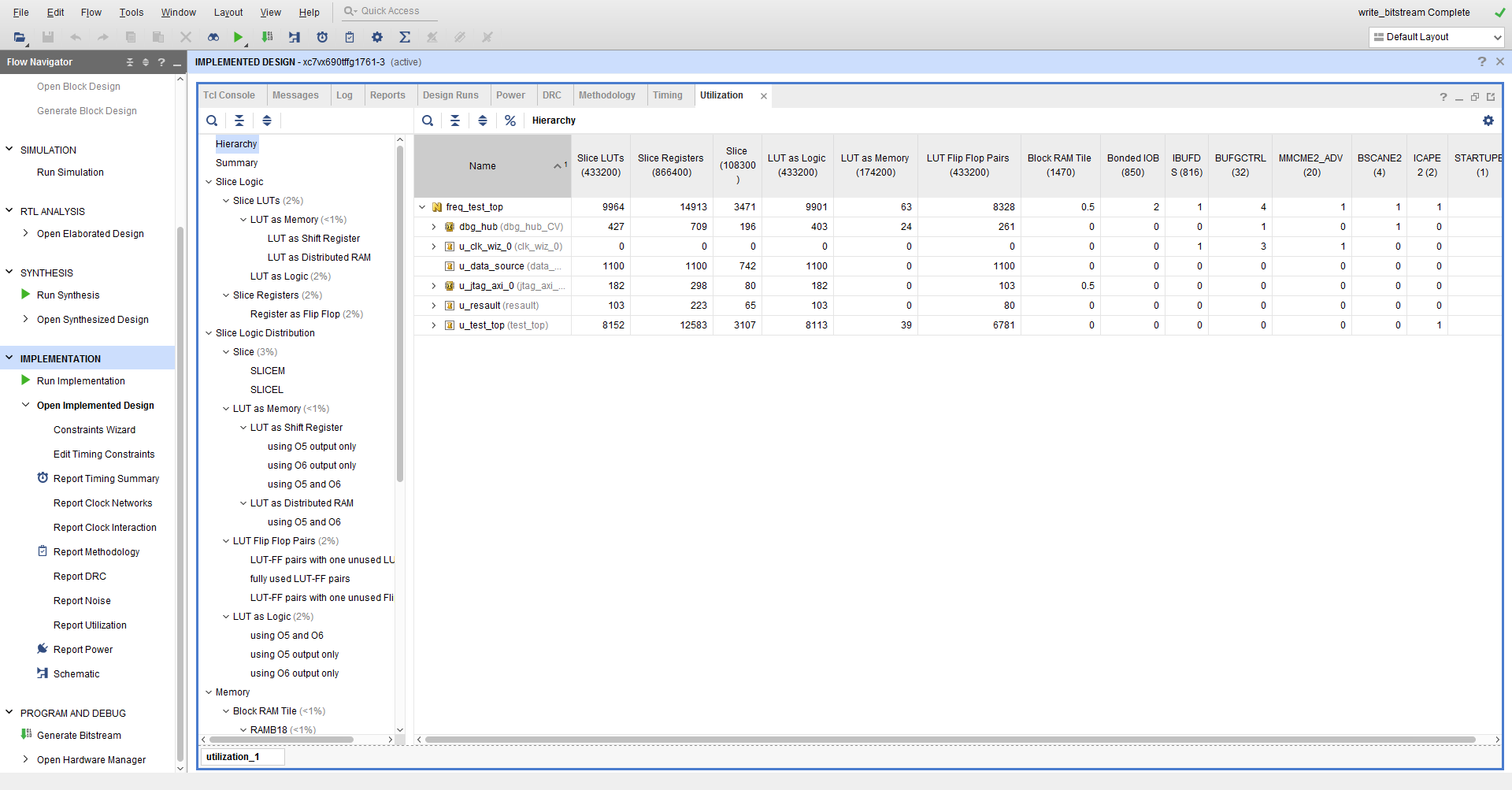


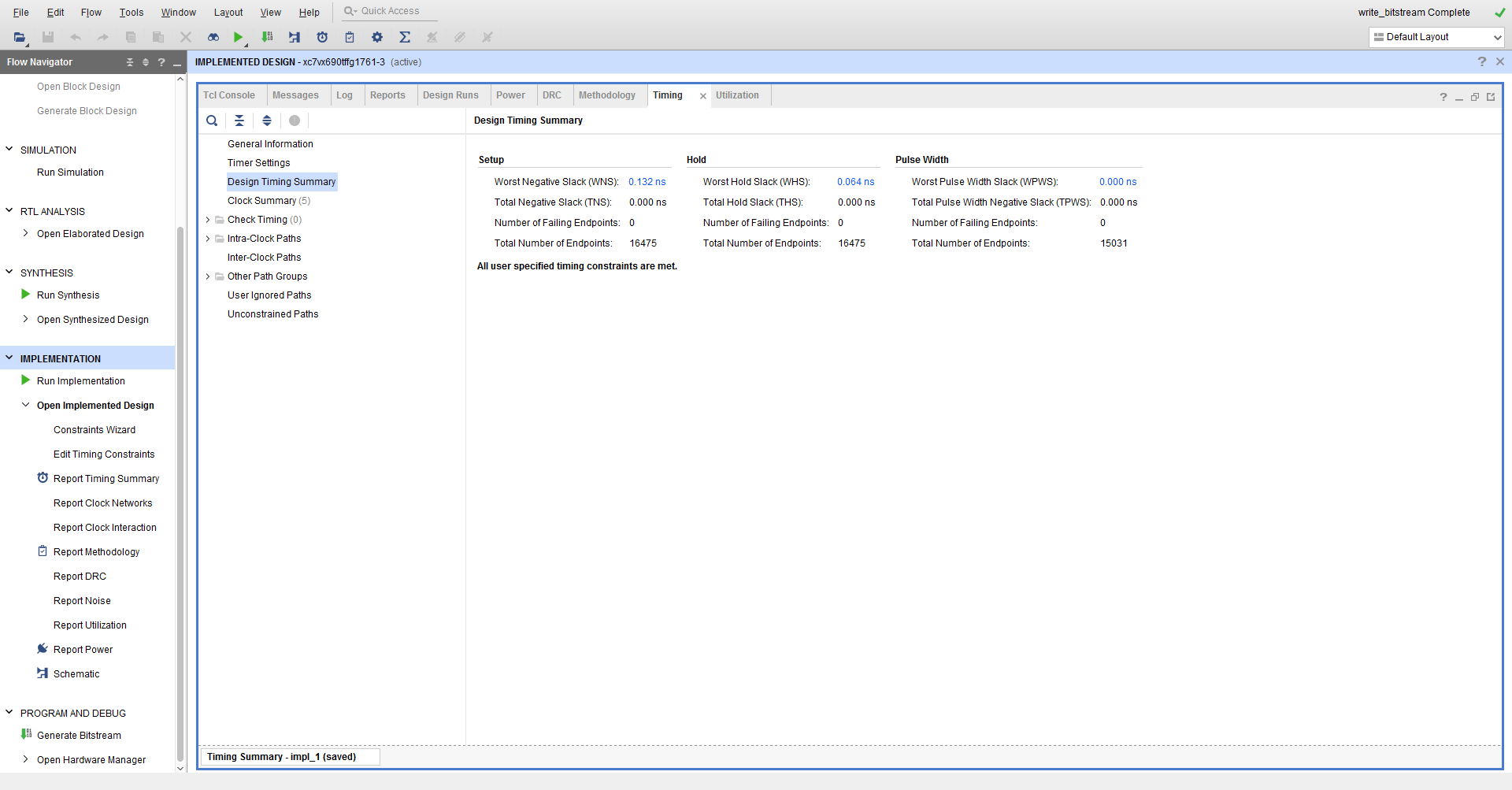
Bus Width：1024bit SEG Width：128bit



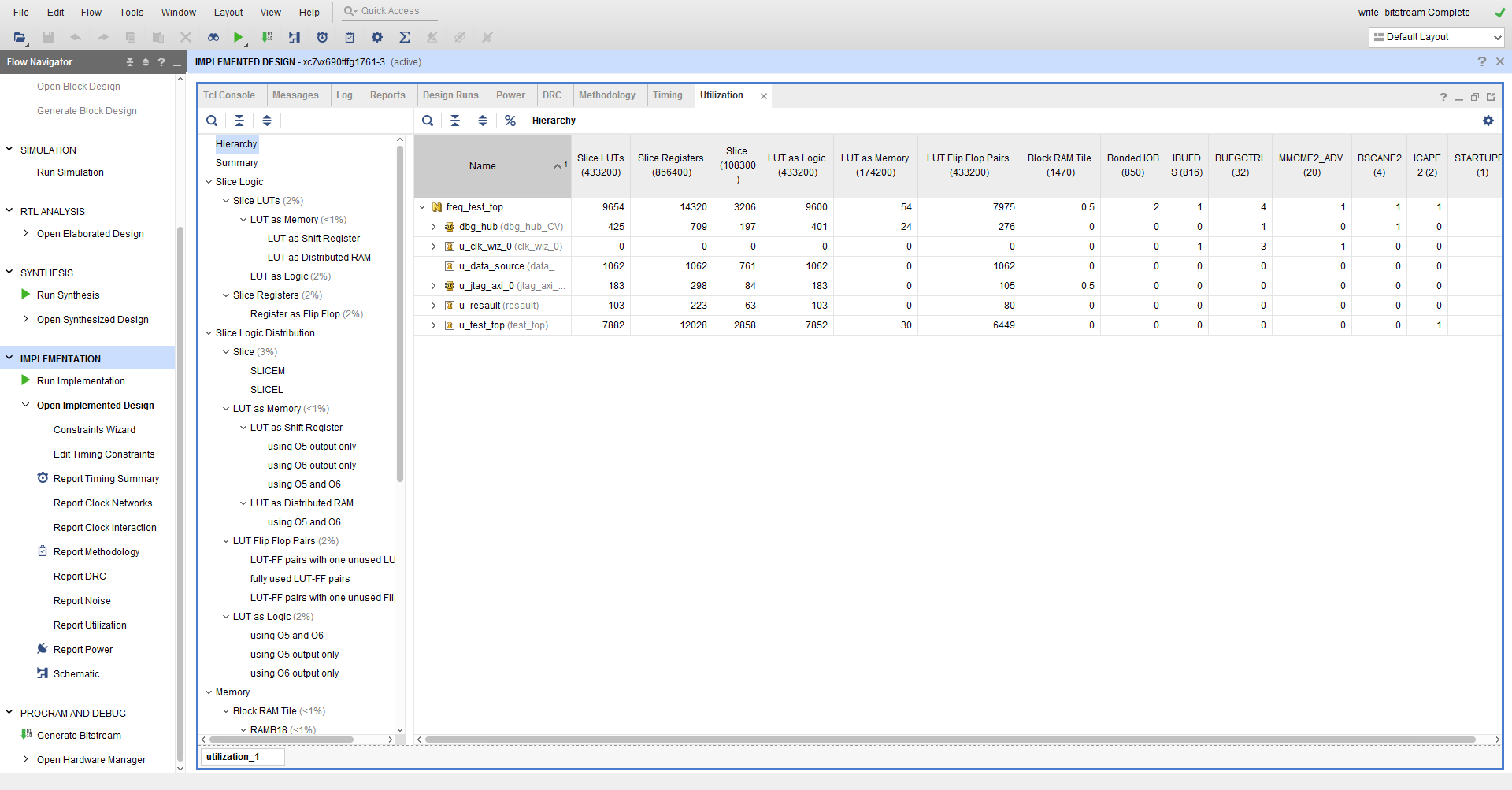


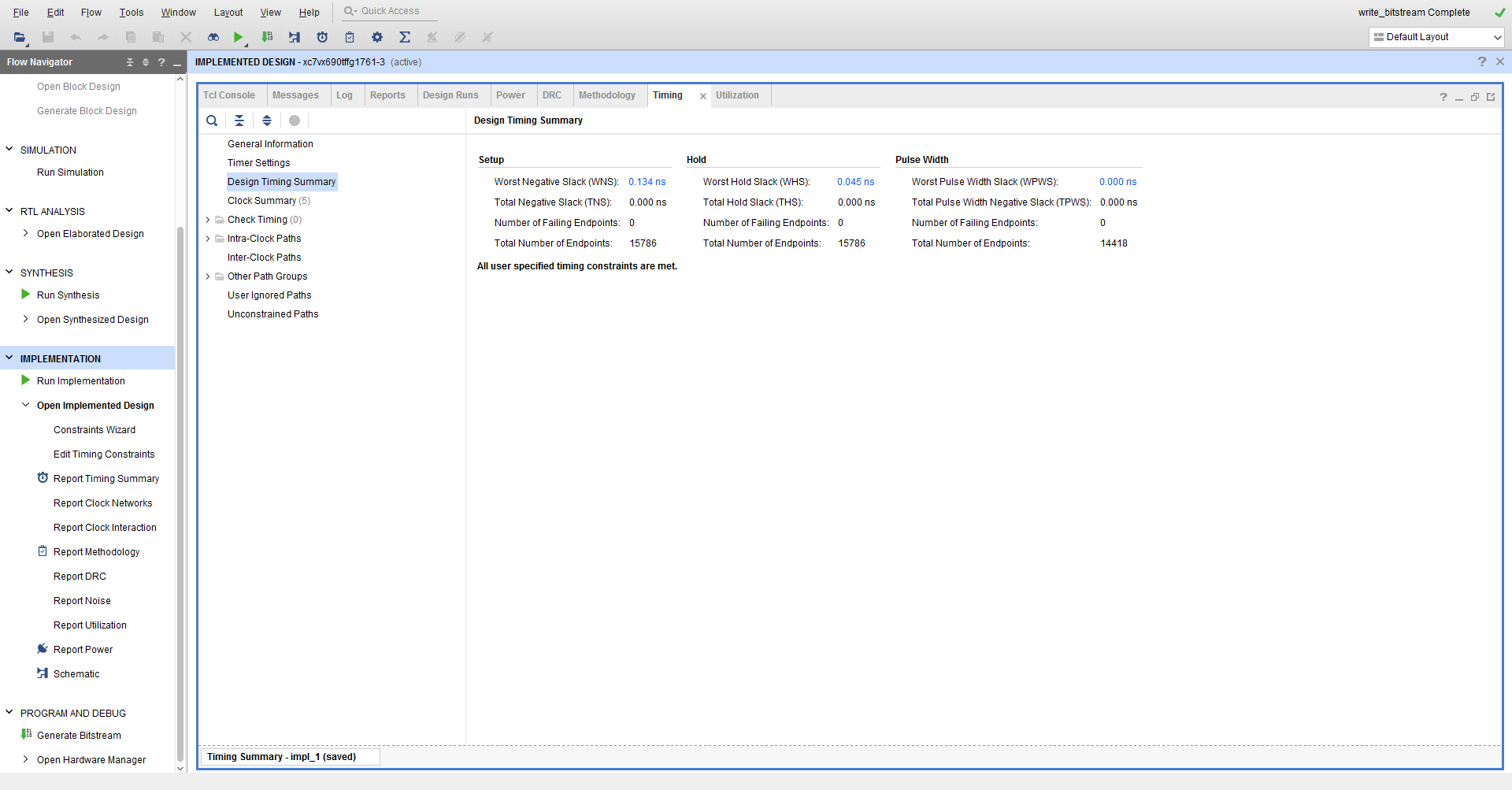
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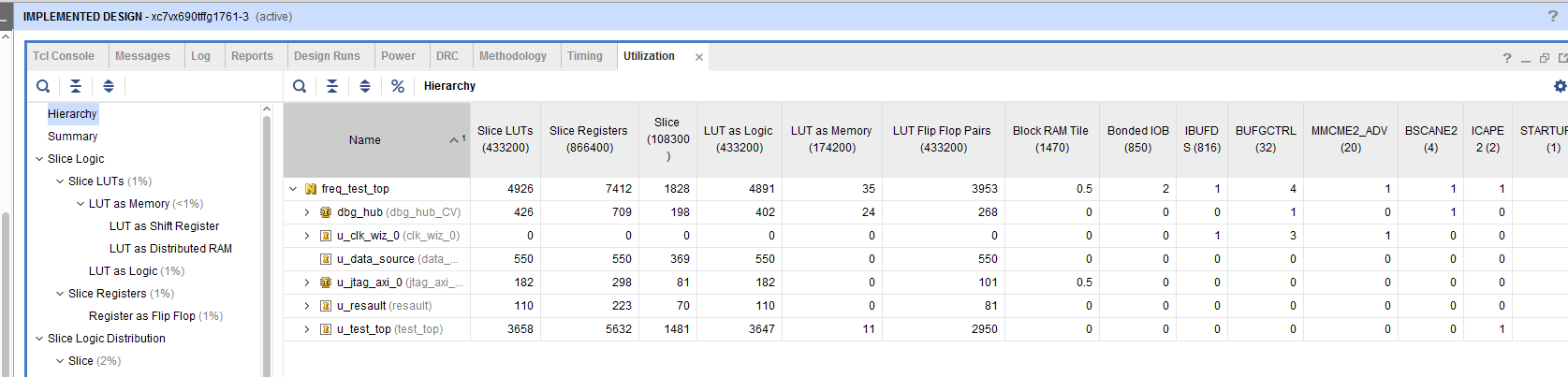


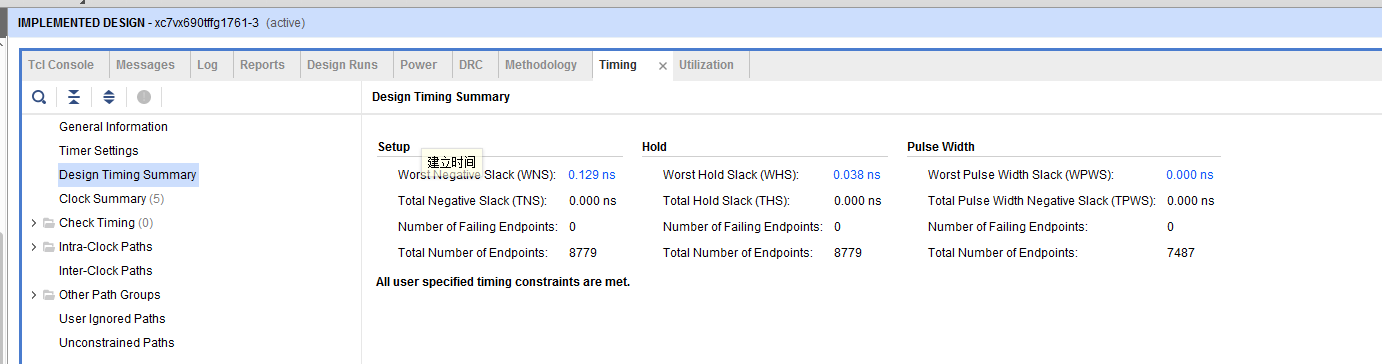
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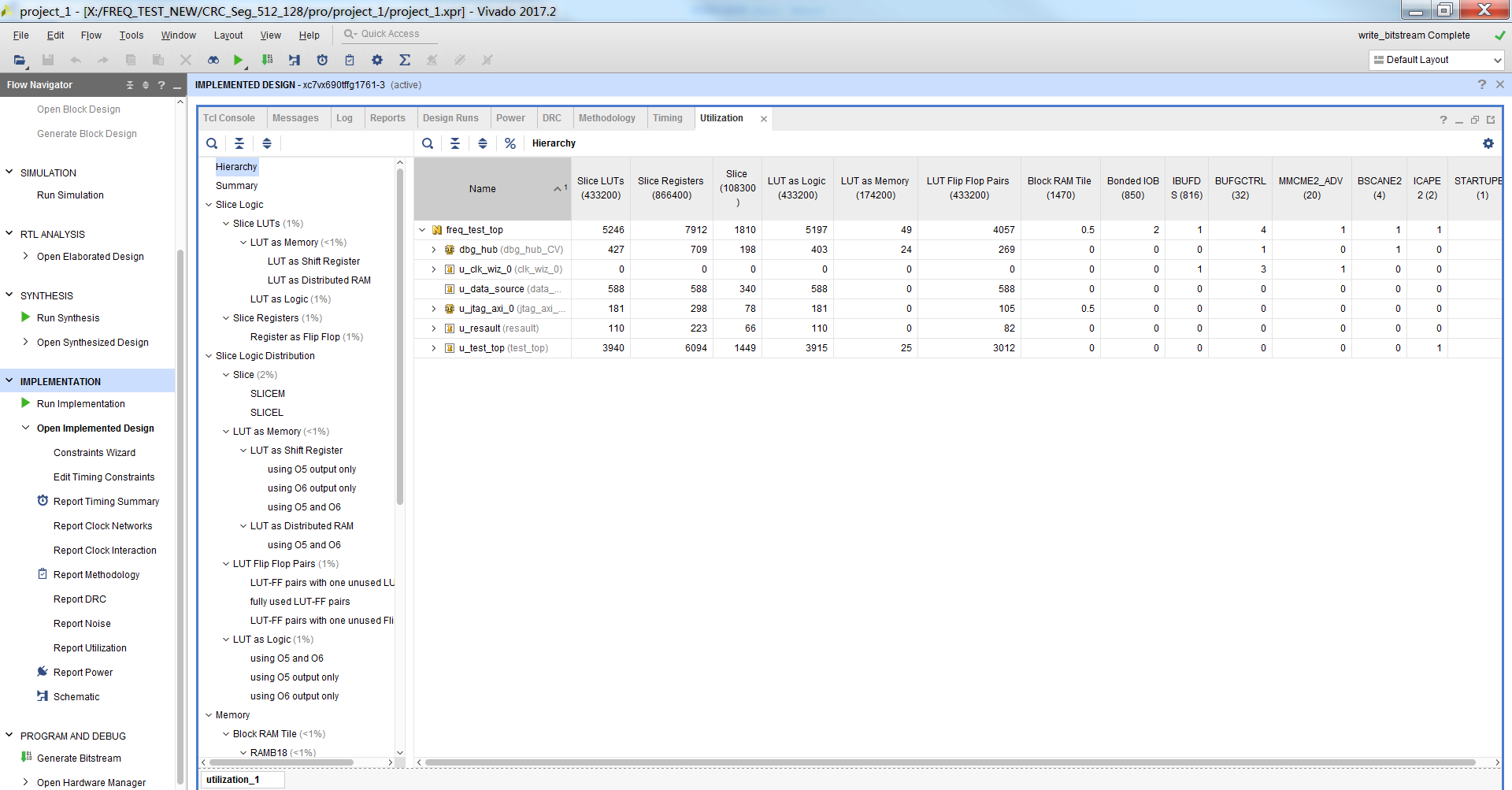


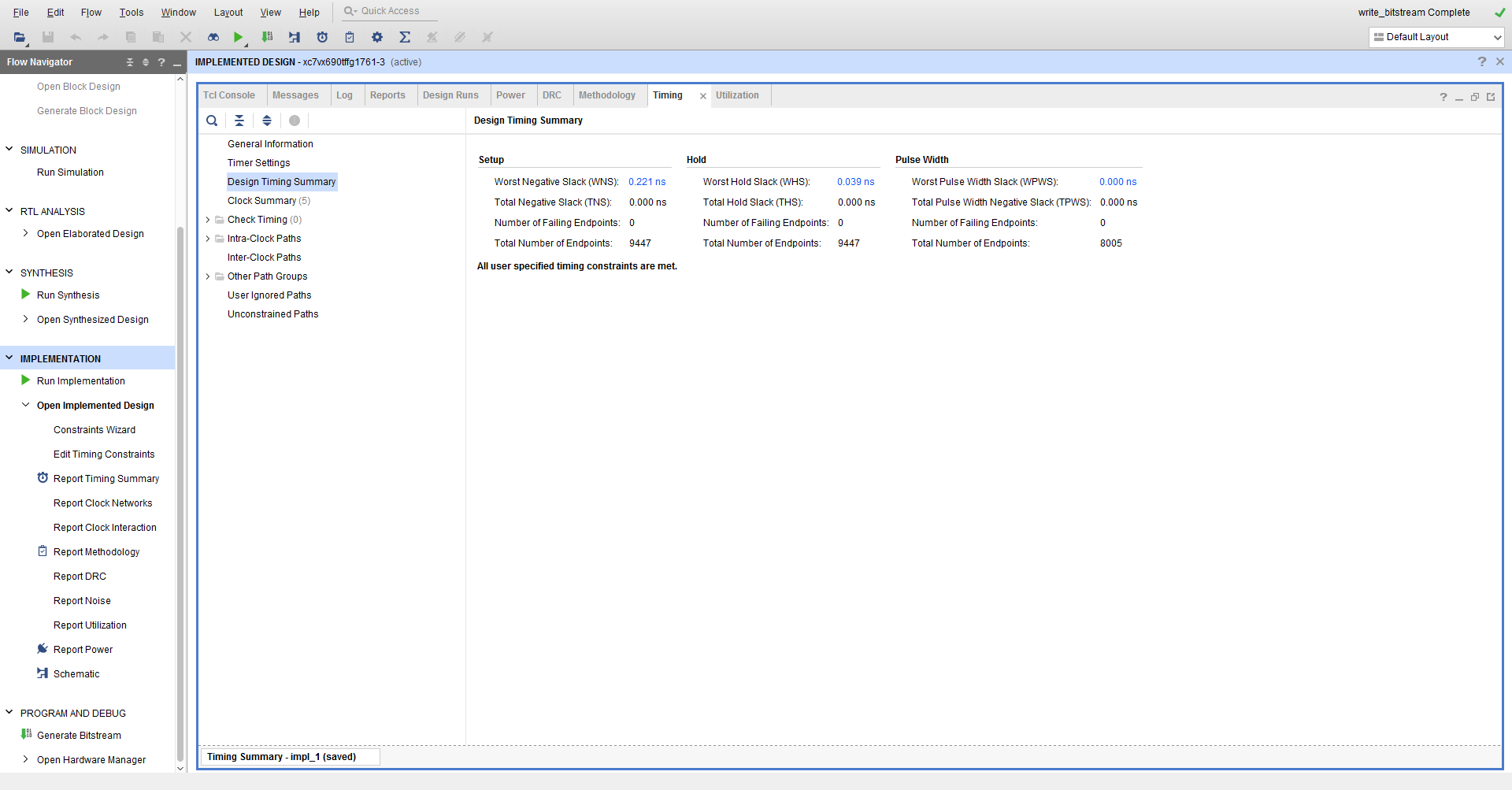
Bus Width：512bit SEG Width：64bit





Bus Width：512bit SEG Width：128bit





Bus Width：512bit SEG Width：256bit

